

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	3	"6172970".pn. or "6754511".pn. or "6058318".pn.	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/11/18 15:11
L2	0	1 and (FIR\$1 or IIR\$1 or channel adj2 response\$1)	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/11/18 15:13
L3	1	"6600935".pn.	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/11/18 15:13
L4	0	3 and (FIR\$1 or IIR\$1 or channel adj2 response\$1)	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/11/18 15:13
L5	12	(Channel adj2 response\$1) with (multi\$3 adj2 antenn\$2 or plural\$3 adj2 antenn\$3 or parallel adj2 antenn\$3 or diversity adj2 antenn\$3 or first adj2 antenn\$3 near3 second adj2 antenn\$3) with (weight\$3)	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/11/18 16:23
L6	0	(Channel adj2 response\$1) with (multi\$3 adj2 antenn\$2 or plural\$3 adj2 antenn\$3 or parallel adj2 antenn\$3 or diversity adj2 antenn\$3 or first adj2 antenn\$3 near3 second adj2 antenn\$3) near3 (combin\$3 or add\$3 or summ\$3) with(weight\$3)	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/11/18 16:30
L7	122	(multi\$3 adj2 antenn\$2 or plural\$3 adj2 antenn\$3 or parallel adj2 antenn\$3 or diversity adj2 antenn\$3 or first adj2 antenn\$3 near3 second adj2 antenn\$3) near3 (combin\$3 or add\$3 or summ\$3) with(weight\$3)	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/11/18 16:10
L8	0	(Channel adj2 response\$1) with (optimiz\$3 near3 interference or maxim\$6 near3 interference or reduc\$4 near3 interference ) with (phase\$1 near2 array\$3)	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/11/18 16:13
L9	0	(Channel adj2 response\$1) with (weight\$3 ) with (phase\$1 near2 array\$3)	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/11/18 16:13
L10	0	(Channel adj2 response\$1)same (weight\$3 ) same (phase\$1 near2 array\$3)	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/11/18 16:14
L11	33	(Channel adj2 response\$1)same (weight\$3 near2 antenn\$3 )	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/11/18 16:14

Day : Friday  
Date: 11/18/2005

Time: 18:53:24

**PALM INTRANET****Inventor Name Search Result**

Your Search was:

Last Name = BINSHTOK

First Name = NIR

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">09976200</a>	Not Issued	71	10/11/2001	Interference reduction using low complexity antenna array	BINSHTOK, NIR
<a href="#">10260856</a>	Not Issued	71	09/30/2002	Method and apparatus for reducing DC offset in a wireless receiver	BINSHTOK, NIR
<a href="#">10430083</a>	Not Issued	41	05/05/2003	Dual antenna system having one phase lock loop	BINSHTOK, NIR

**Inventor Search Completed: No Records to Display.**

**Search Another: Inventor**

<b>Last Name</b>	<b>First Name</b>
<input type="text" value="BINSHTOK"/>	<input type="text" value="NIR"/>
<input type="button" value="Search"/>	

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L12	467	(Channel adj2 response\$1)same (phase\$1 near2 array\$3 or array\$3 )	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/11/18 16:18
L13	14	11 and 12	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/11/18 16:15
L14	28	(Channel adj2 response\$1 or transfer adj2 function\$1) same (phase\$1 near2 array\$3 or array\$3 ) same (weight\$3 near3 antenn\$3)	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/11/18 16:19
L15	18	14 not 13	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/11/18 16:19
L16	21	(multi\$3 adj2 antenn\$2 or plural\$3 adj2 antenn\$3 or parallel adj2 antenn\$3 or diversity adj2 antenn\$3 or first adj2 antenn\$3 near3 second adj2 antenn\$3 or antenn\$3 near2 array\$3) near3 (combin\$4 or summ\$3 or add\$3) with (single adj2 path\$1 or single adj2 receive\$3 or single adj2 RF or one adj2 path\$1)	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/11/18 16:25
L17	3	binshtok-nir.in.	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/11/18 16:29
L18	54	yellin-daniel.in.	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/11/18 16:29
L19	1	11 and 18	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/11/18 16:29
L20	10	18 and (multi\$3 adj2 antenn\$2 or plural\$3 adj2 antenn\$3 or parallel adj2 antenn\$3 or diversity adj2 antenn\$3 or first adj2 antenn\$3 near3 second adj2 antenn\$3)	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/11/18 16:30

Day : Friday  
Date: 11/18/2005

Time: 18:53:39



# PALM INTRANET

## Inventor Name Search Result

Your Search was:

Last Name = YELLIN

First Name = DANIEL

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">09301116</a>	<a href="#">6314130</a>	150	04/28/1999	SYSTEM AND METHOD FOR JOINT TIME TRACKING OF MULTIPLE PATHS	YELLIN', DANIEL
<a href="#">08043089</a>	<a href="#">5539832</a>	150	04/05/1993	MULTI-CHANNEL SIGNAL SEPARATION USING CROSS-POLYSPECTRA	YELLIN, DANIEL
<a href="#">08873880</a>	<a href="#">6034986</a>	150	06/11/1997	METHOD AND APPARATUS FOR PRODUCING SPREAD SPECTRUM NOISE	YELLIN, DANIEL
<a href="#">09103683</a>	<a href="#">6047035</a>	150	06/15/1998	METHOD AND DEVICE FOR QUANTIZING THE INPUT TO SOFT DECODERS	YELLIN, DANIEL
<a href="#">09156891</a>	<a href="#">6208684</a>	150	09/18/1998	CYCLIC ADAPTIVE RECEIVERS FOR DS-CDMA SIGNALS	YELLIN, DANIEL
<a href="#">09357235</a>	<a href="#">6459398</a>	150	07/20/1999	PULSE MODULATED DIGITAL TO ANALOG CONVERTER (DAC)	YELLIN, DANIEL
<a href="#">09371275</a>	<a href="#">6848069</a>	150	08/10/1999	ITERATIVE DECODING PROCESS	YELLIN, DANIEL
<a href="#">09371276</a>	<a href="#">6606490</a>	150	08/10/1999	BATTERY OPERATED RADIO RECEIVERS HAVING POWER SAVE BY REDUCING ACTIVE RECEPTION TIME	YELLIN, DANIEL
<a href="#">09387310</a>	<a href="#">6870890</a>	150	08/31/1999	POWER SAVING IN COMMUNICATION TERMINALS	YELLIN, DANIEL
<a href="#">09443864</a>	<a href="#">6628701</a>	150	11/19/1999	METHOD AND APPARATUS FOR REDUCING SPREAD SPECTRUM NOISE	YELLIN, DANIEL
<a href="#">09493004</a>	<a href="#">6895541</a>	150	01/28/2000	METHOD AND DEVICE FOR QUANTIZING THE INPUT TO SOFT DECODERS	YELLIN, DANIEL

<u>09511737</u>	<u>6963889</u>	150	02/24/2000	WAVE DIGITAL FILTER WITH LOW POWER CONSUMPTION	YELLIN, DANIEL
<u>09550855</u>	<u>6741662</u>	150	04/17/2000	TRANSMITTER LINEARIZATION USING FAST PREDISTORTION	YELLIN, DANIEL
<u>09621388</u>	<u>6898252</u>	150	07/21/2000	IQ MISMATCH CANCELLATION	YELLIN, DANIEL
<u>09633131</u>	<u>6618433</u>	150	08/04/2000	FAMILY OF LINEAR MULTI-USER DETECTORS (MUDS)	YELLIN, DANIEL
<u>09649590</u>	<u>6546042</u>	150	08/29/2000	CYCLIC ADAPTIVE RECEIVERS FOR DS-CDMA SIGNALS	YELLIN, DANIEL
<u>09661127</u>	<u>6801581</u>	150	09/13/2000	DC OFFSET CANCELLATION	YELLIN, DANIEL
<u>09680665</u>	<u>6895421</u>	150	10/06/2000	METHOD AND APPARATUS FOR EFFECTIVELY PERFORMING LINEAR TRANSFORMATIONS	YELLIN, DANIEL
<u>09860032</u>	Not Issued	95	05/17/2001	REDUCING SPREAD SPECTRUM NOISE	YELLIN, DANIEL
<u>09880707</u>	Not Issued	61	06/12/2001	Low complexity channel decoders	YELLIN, DANIEL
<u>09966736</u>	<u>6674793</u>	150	10/01/2001	SYSTEM AND METHOD FOR JOINT TIME TRACKING OF MULTIPLE PATHS	YELLIN, DANIEL
<u>09966753</u>	Not Issued	51	10/01/2001	System and method for joint time tracking of multiple paths	YELLIN, DANIEL
<u>09974921</u>	Not Issued	41	10/10/2001	Determining cross-correlation between spreading sequences	YELLIN, DANIEL
<u>09976200</u>	Not Issued	71	10/11/2001	Interference reduction using low complexity antenna array	YELLIN, DANIEL
<u>09981631</u>	Not Issued	94	10/16/2001	TIME DELAY ESTIMATION IN A TRANSMITTER	YELLIN, DANIEL
<u>09983775</u>	Not Issued	93	10/25/2001	METHOD AND APPARATUS FOR REDUCING SPREAD SPECTRUM NOISE	YELLIN, DANIEL
<u>09990035</u>	Not Issued	93	11/21/2001	LOW COMPLEXITY MULTIUSER DETECTOR	YELLIN, DANIEL
<u>10039072</u>	Not Issued	71	01/02/2002	Robust low complexity multi-antenna adaptive minimum mean square error equalizer	YELLIN, DANIEL
<u>10053490</u>	Not Issued	61	10/26/2001	Metric correction for multi user detection, for long codes DS-CDMA	YELLIN, DANIEL

<u>10056983</u>	Not Issued	71	01/25/2002	Novel receiver architecture for CDMA receiver downlink	YELLIN, DANIEL
<u>10107527</u>	Not Issued	30	03/25/2002	Processing non-pilot channels in a CDMA searcher	YELLIN, DANIEL
<u>10291189</u>	Not Issued	30	11/08/2002	Reduced complexity MMSE multiuser detection for a multirate CDMA link	YELLIN, DANIEL
<u>10318879</u>	Not Issued	41	12/13/2002	Cellular system with link diversity feedback	YELLIN, DANIEL
<u>10334529</u>	<u>6876695</u>	150	12/31/2002	METHOD AND APPARATUS FOR REDUCING SPREAD SPECTRUM NOISE	YELLIN, DANIEL
<u>10449131</u>	<u>6782250</u>	150	06/02/2003	BATTERY OPERATED RADIO RECEIVERS HAVING POWER SAVE BY REDUCING ACTIVE RECEPTION TIME	YELLIN, DANIEL
<u>10632843</u>	Not Issued	30	08/04/2003	Channel estimator	YELLIN, DANIEL
<u>10673267</u>	Not Issued	30	09/30/2003	Method and apparatus for multi-algorithm detection	YELLIN, DANIEL
<u>10697853</u>	Not Issued	30	10/30/2003	Unified MMSE equalization and multi-user detection approach for use in a CDMA system	YELLIN, DANIEL
<u>10734117</u>	Not Issued	30	12/15/2003	Filter for a modulator and methods thereof	YELLIN, DANIEL
<u>10829075</u>	Not Issued	20	04/21/2004	Low complexity channel decoders	YELLIN, DANIEL
<u>10924996</u>	Not Issued	30	08/25/2004	DC offset cancellation	YELLIN, DANIEL
<u>10930857</u>	Not Issued	71	09/01/2004	Apparatus and method of adaptive filter	YELLIN, DANIEL
<u>10986057</u>	Not Issued	30	11/12/2004	Iterative decoding process	YELLIN, DANIEL
<u>11056226</u>	Not Issued	20	02/14/2005	Apparatus and method of canceling interference	YELLIN, DANIEL
<u>11099838</u>	Not Issued	20	04/05/2005	Method and apparatus for effectively performing linear transformations	YELLIN, DANIEL
<u>11124586</u>	Not Issued	30	05/09/2005	System, apparatus and method of varying channel bandwidth	YELLIN, DANIEL
<u>11136476</u>	Not Issued	30	05/25/2005	Device, system and method of multiple access transmission	YELLIN, DANIEL
<u>60243291</u>	Not Issued	159	10/26/2000	Method and apparatus for channel decoders	YELLIN, DANIEL

<a href="#">60622271</a>	Not Issued	159	10/25/2004	Model driven development products	YELLIN, DANIEL
<a href="#">09696137</a>	<a href="#">6636875</a>	150	10/25/2000	SYSTEM AND METHOD FOR SYNCHRONIZING RELATED DATA ELEMENTS IN DISPARATE STORAGE SYSTEMS	YELLIN, DANIEL M.

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<input type="text" value="YELLIN"/>	<input type="text" value="DANIEL"/>

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